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LSI Logic Corporation Legal Department - IP 1621 Barber Lane, MS D-106 Milpitas, CA 95035

Facsimile Transmittal Letter

Date	February 2, 2006		
Number	of pages including cover sheet	3_	

To: US PTO, Mail Stop IDS

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TC 2825

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REMARKS:							
	gent 🔲	For your review	•	Reply ASAP		Please comment	
Application Number: Filing date: First named inventor: Attorney docket number:	10/719,393 November 21, Valeri Kudry: 03-0937 / L1:	avtşev et al.					
Transmitted herewith for filing via facsimile:							
 Information Disclo 	sure Statement I	_etter					
 Information Disclo 	sure Statement b	y Applicant PTO/S	SB/O8	BA			

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

16:26

Valeri Kudryavtsev et. al.

Serial No.:

10/719,393

Filed:

November 21, 2003

For:

Process and Apparatus for Placement of Megacells in ICs

Design

§ Ş 8 §

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Group Art Unit:

2825

FEB 0 2 2006

Examiner:

Doan, Nghia M.

Atty Docket:

L13.12-0257 / 03-

0937

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I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. Q. Box 1450, Alexandria, VA 22313-1450, on the date below:

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

266806

Respectfully submitted, world

Timothy Croll

Reg. No. 36,771

16:26

PTO/SB/08A (10-96)
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INICO	DRAATIC	N DICC	OCUDE	Application Number	10/719,393	
INFORMATION DISCLOSURE				Filing Date	November-21, 2003	
STATEMENT BY APPLICANT		First Named Inventor	Alexei Galatenko			
				Group Art Unit	2825	
				Examiner Name	Nghia M. Doan	
Sheet	1	l of	1	Attorney Docket No.	1 12 12 0257 / 03 0997	フ

U.S. PATENT DOCUMENTS					
Examiner Initials		U.S. Patent Docum Number Kind C	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		6,725,432	Chang et al.	04-20-2004	

Ex	aminer	Date]
Sic	nature	considered	
1-15	******		